

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rajesh Khamankar, et al.
Serial No.: 10/702,234
Filed: November 6, 2003
Title: **RELIABLE HIGH VOLTAGE GATE DIELECTRIC
LAYERS USING A DUAL NITRIDATION PROCESS**
Grp./A.U.: 2814
Examiner: Nathan W. Ha Confirmation No. 7943

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I her-by certify that this correspondence is being electronically filed
with United States Patent and trademark Office on:

August 9, 2006(Date)

Elizabeth Schumacher

(Printed or typed name of person signing the certificate)

/Elizabeth Schumacher/

(Signature of the person signing the certificate)

Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

In response to the Examiner's Action mailed March 9, 2006, please amend the above-
identified application as follows: